



COPY OF PAPERS
ORIGINALLY FILED

#12
formal
dwgs
PATENT 9-19-02
ORJ

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Protigal et al.

Serial No.: 09/776,387

Filed: February 2, 2001

For: INTEGRATED CIRCUIT MODULE
HAVING ON-CHIP SURGE CAPACITORS

Examiner: A. Sefer

Group Art Unit: 2826

Attorney Docket No.: 2898.2US
(88-070.7)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

July 29, 2002
Date of Deposit

Signature of registered practitioner or other person having reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

Joseph A. Walkowski
Typed/printed name of person whose signature is contained above

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
Washington, D.C. 20231

Sir:

Attached please find corrected formal drawings for this application.

Respectfully submitted,

Joseph A. Walkowski
Registration No. 28,765
Attorney for Applicants
TRASKBRITT, PC
P. O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: (801) 532-1922

RECEIVED
SEP 17 2002
TECHNOLOGY CENTER 2800

Date: July 29, 2002

JAW/ps:dlm

N:\2269\2898.2\Transmittal of Frml Dwgs.wpd